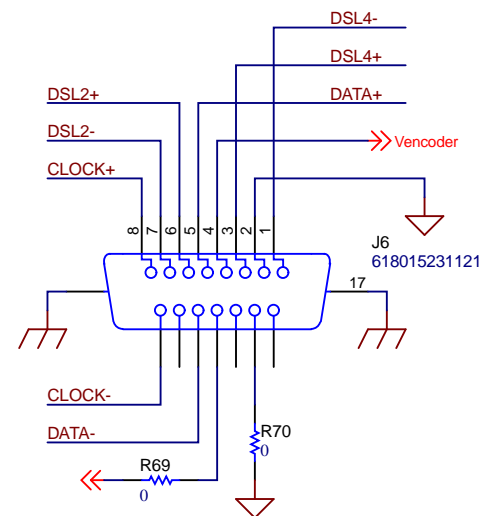
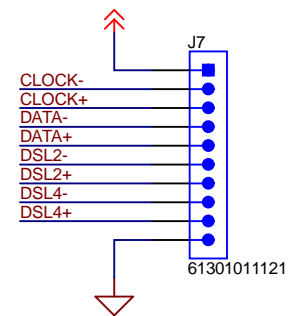
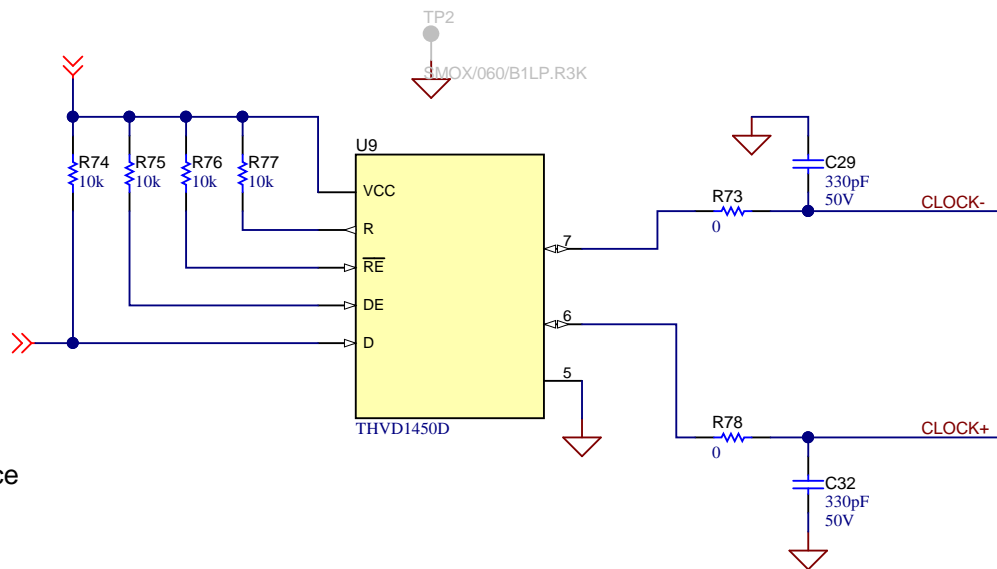
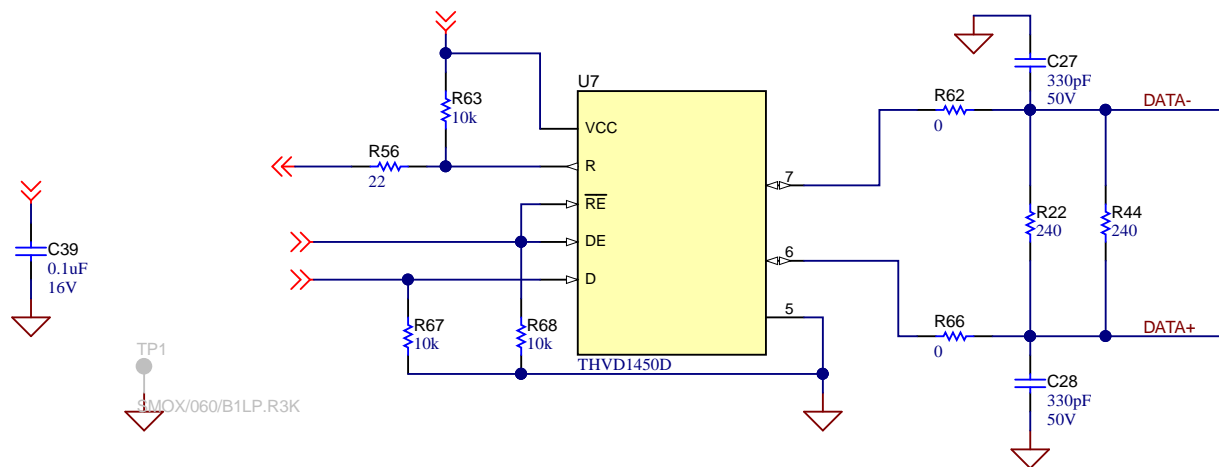


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Revision History	
Revision	Notes

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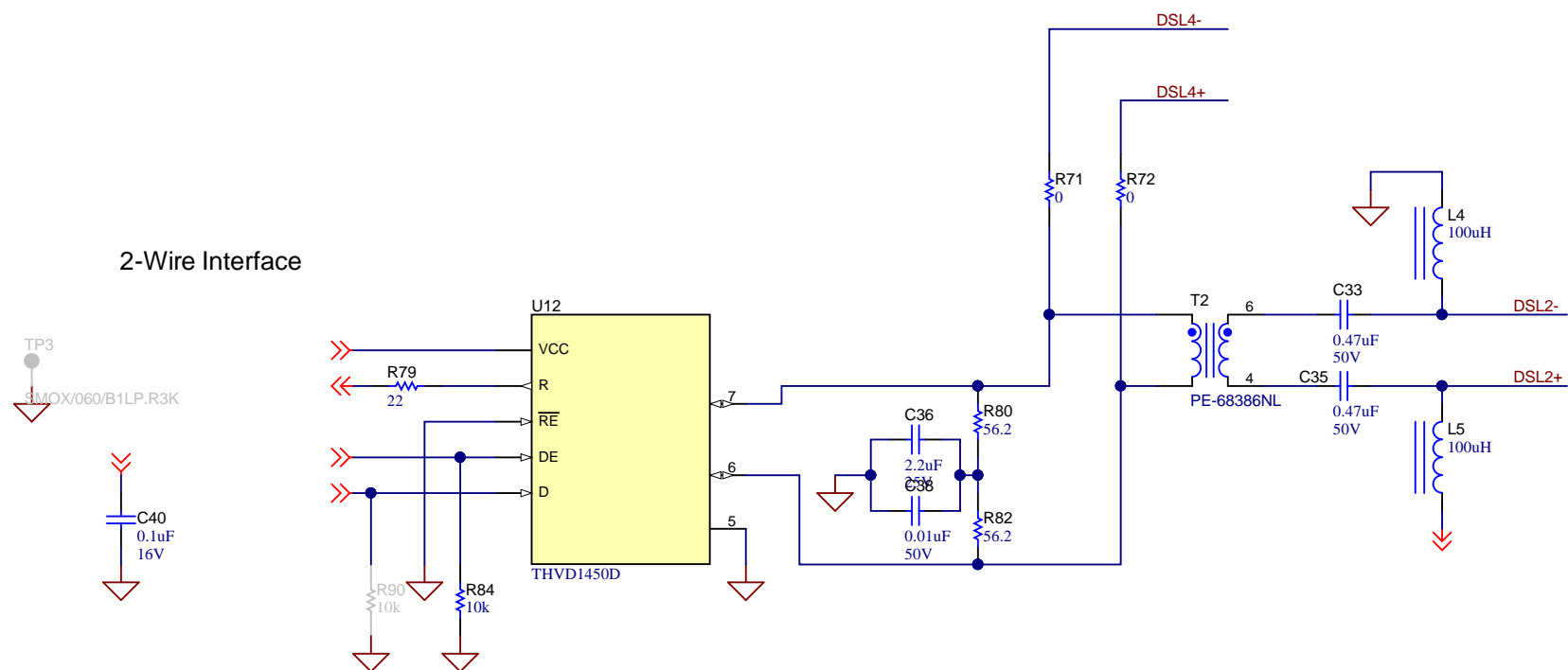
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TID #: TIDA-00179	Project Title: Universal Digital Interface to Absolute Encoders	
Number: TIDA-00179	Rev: E2.1	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: Production	Sheet: 1 of 3
Drawn By:	File: CoverSheet.SchDoc	Size: B
Engineer: V.Pizzolante and K. Mogen	Contact: http://www.ti.com/support	



4-Wire Interface

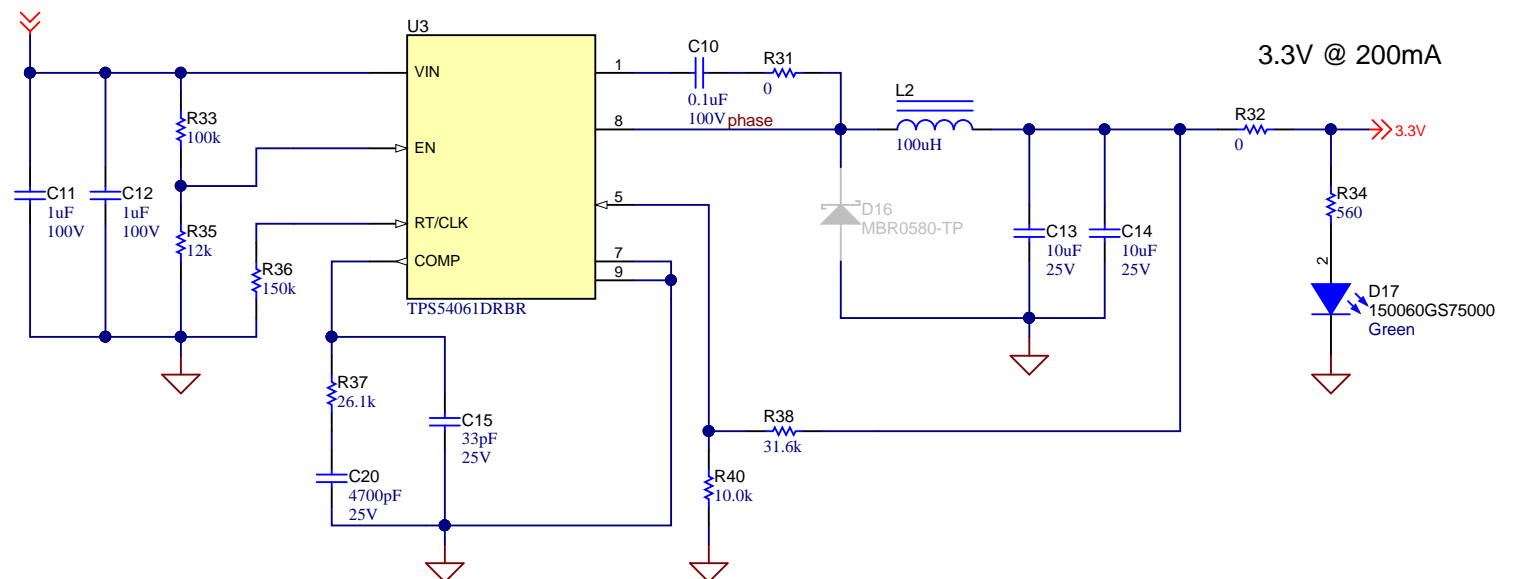
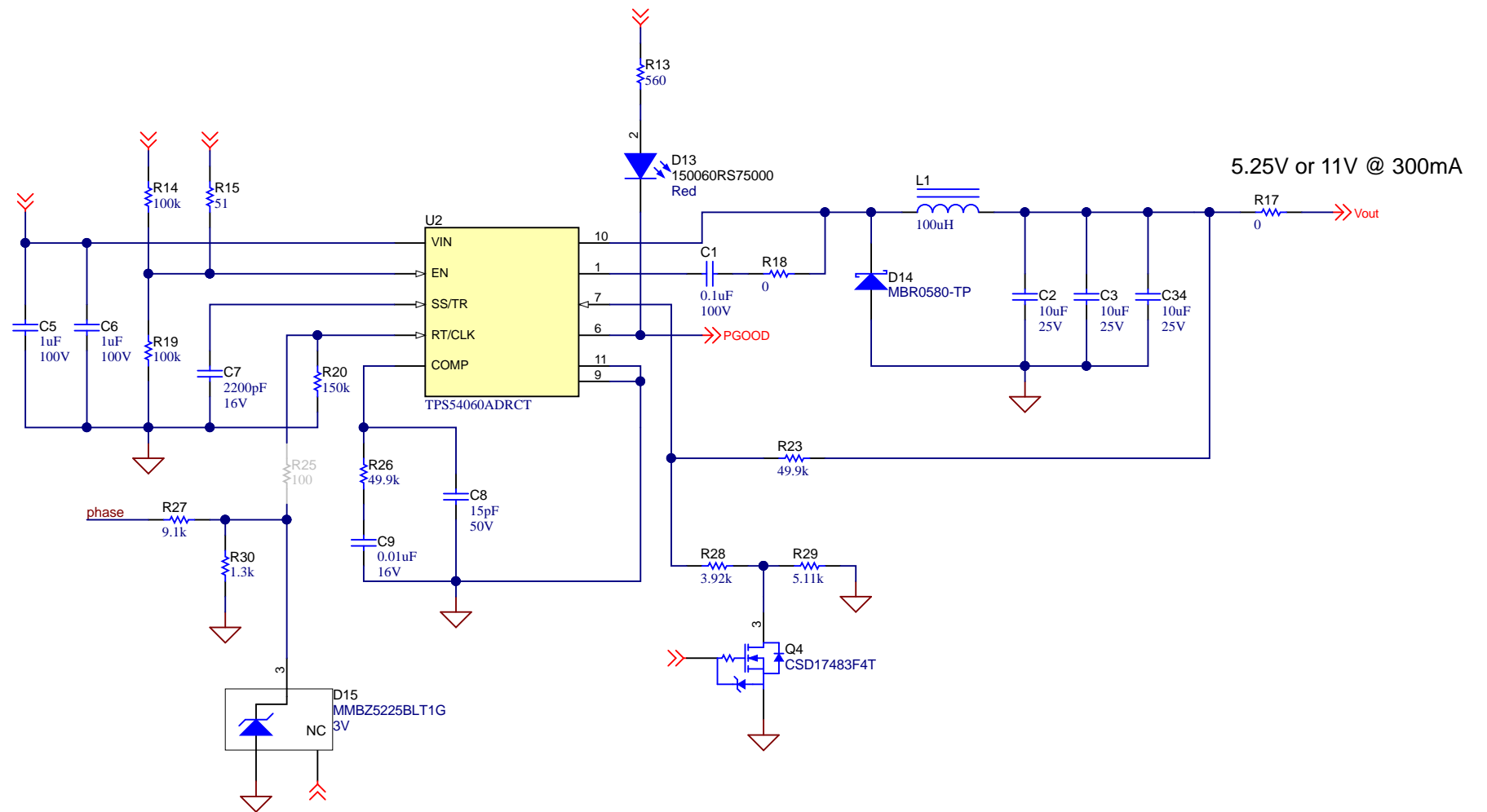
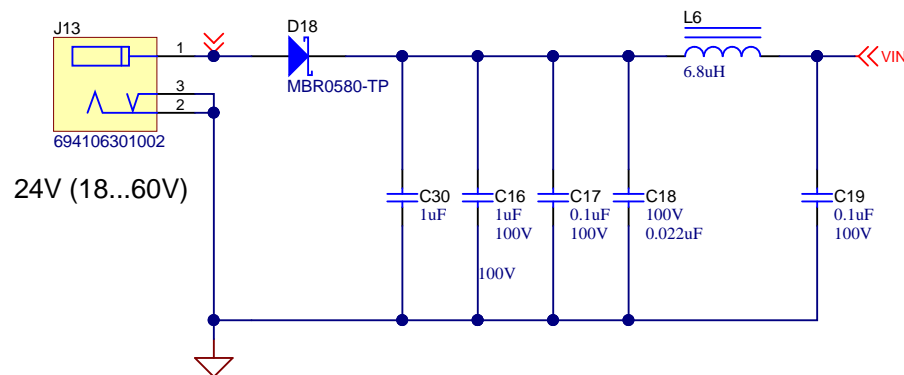
Encoder Connectors

2-Wire Interface

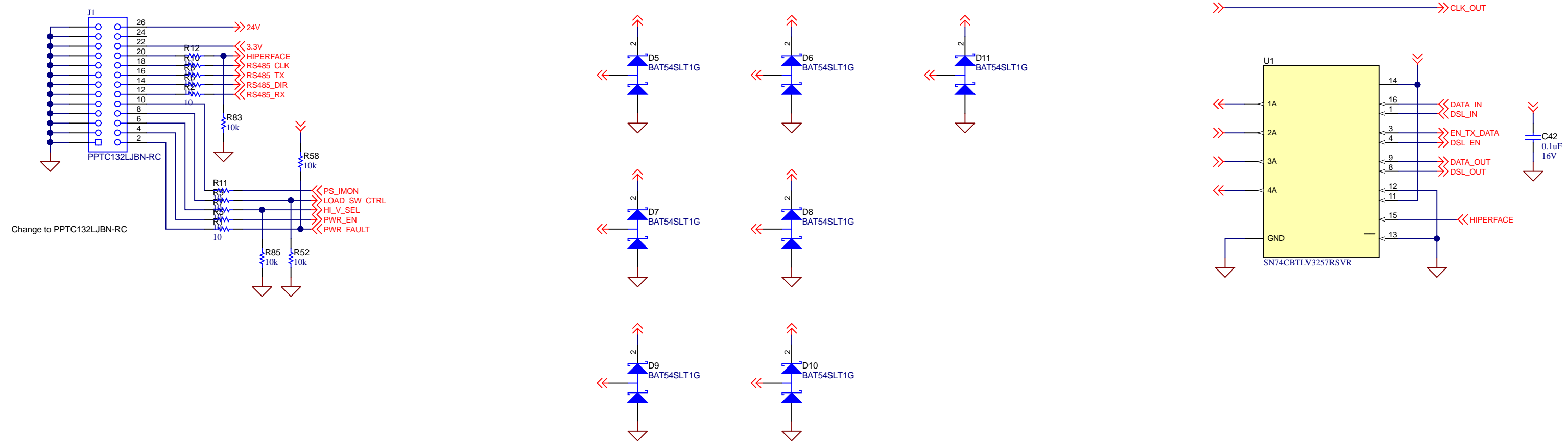


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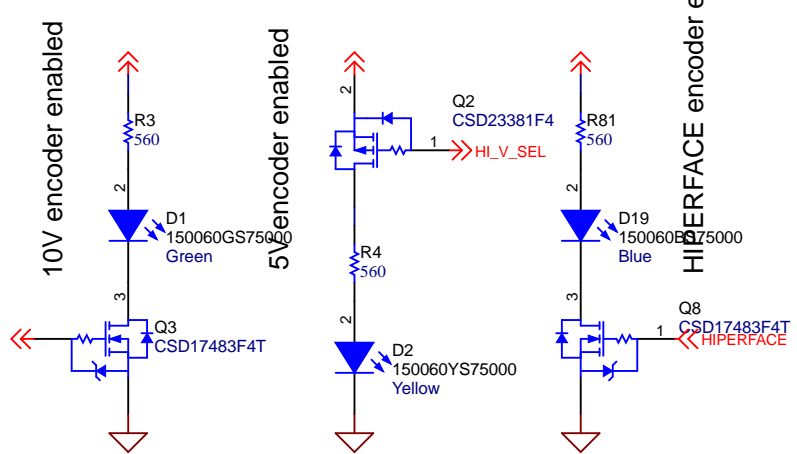
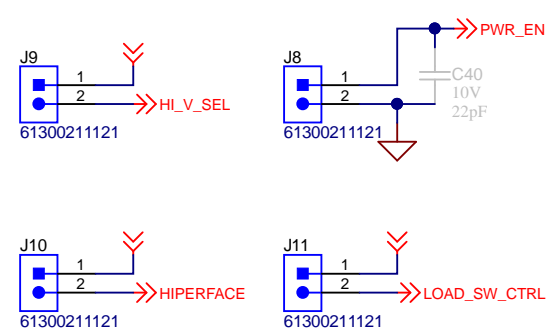
Host Processor Interface Connectors

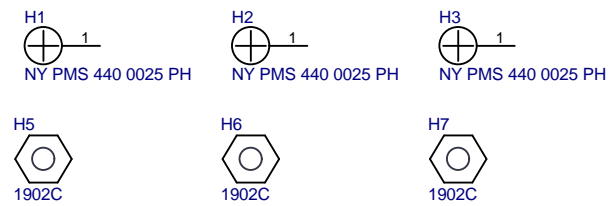


Input ESD protection

LED status indicators

Configuration Settings





PCB Number: TIDA-00179
PCB Rev: E2.1

Printed Circuit Board

Logo1
PCB
LOGO
Texas Instruments

Logo2
PCB
LOGO
Pb-Free Symbol

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WEEE logo

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Orderable: N/A	Designed for: Public Release	Mod. Date: 1/21/2019
TID #: TIDA-00179	Project Title: Universal Digital Interface to Absolute Encoders	
Number: TIDA-00179	Rev: E2.1	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: Production	Sheet: 3 of 3
Drawn By:	File: TID_Hardware.SchDoc	Size: B
Engineer: V.Pizzolante and K. Mogen	Contact: http://www.ti.com/support	